

05-05-05

3 May 2005

(8)

Z/W AF/2814 #

MAIL STOP AF  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, Virginia 22313-1450

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation  
Assignee(s): National Semiconductor Corporation

Sir:

Transmitted herewith are the following documents for the above patent application:

1. Return Receipt Postcard;
2. This Transmittal Letter (in duplicate);
3. Amendment Under 37 CFR 1.116 (50 pp.).

☒ The fee has been calculated as shown below:**CLAIMS AS AMENDED**

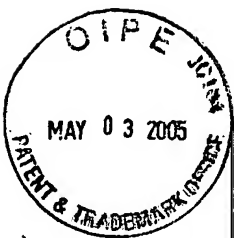
	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>		<u>Rate</u>		<u>Additional Fee</u>
Total Claims	68	Minus	68	=	0	x	\$50.00	\$	0.00
Independent Claims	6	Minus	6	=	0	x	\$200.00	\$	0.00
<input type="checkbox"/>	Fee of \$360 for the first filing of one or more multiple dependent claims							\$	
<input type="checkbox"/>	Fee for Request for Extension of Time (      month(s))							\$	
<input type="checkbox"/>	Fee for							\$	
<b><u>Total additional fee for this Amendment:</u></b>								\$	0.00
<input checked="" type="checkbox"/>	Please charge Deposit Account No. 502641 in the amount of							\$	0.00
<input checked="" type="checkbox"/>	Conditional Petition for Extension of Time: If an extension of time is required, the Commissioner is authorized to deduct the necessary fee from Deposit Account No. 502641.								
<input checked="" type="checkbox"/>	Also, charge any additional fees required and credit any overpayment to Deposit Account No. 502641.								

**EXPRESS MAIL LABEL NO.:****EV 500 310 859 US**

Respectfully submitted,

Ronald J. Meetin  
Attorney for Applicant(s)  
Reg. No. 29,089

210 Central Avenue  
Mountain View, California  
Tel.: 650-964-9767



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/054,653 Confirmation No.: 9448  
First Named Inventor: Bulucea, Constantin Filing Date: 18 January 2002  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127 US  
Title: Gate-Enhanced Junction Varactor With Gradual  
Capacitance Variation  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
3 May 2005

**MAIL STOP AF  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, Virginia 22313-1450**

**AMENDMENT UNDER 37 CFR 1.116**

Sir:

This amendment is tentatively submitted under 37 CFR 1.116 in response to the final Office Action mailed 23 February 2005 for the above patent application. As explained further below, the 23 February 2005 Office Action is incomplete for failure to provide grounds for rejecting certain of the claims. Accordingly, the finality of the February 2005 Office Action is to be withdrawn. The status of the above application is to be returned to non-final status in which amendment of the application is not limited by 37 CFR 1.116. In view of this, please amend the above application in the following manner.

Ronald J. Meetin  
Attorney at Law  
210 Central Avenue  
Mountain View, CA  
94043-4869  
Tel.: 650-964-9767  
Fax: 650-964-9779